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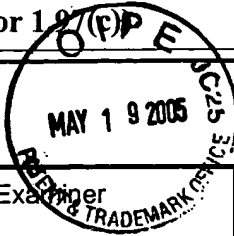
TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))					Docket No. SEC.1065	
In Re Application Of: Sang-Hyeop Lee et al.						
Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.	
10/665,632	09/22/2003	Andy Huynh	20987	2813	7306	
Title: METHOD OF ENCAPSULATING SEMICONDUCTOR DEVICES ON A PRINTED CIRCUIT BOARD, AND A PRINTED CIRCUIT BOARD FOR USE IN THE METHOD						
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450						
37 CFR 1.97(b)						
1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.						
37 CFR 1.97(c)						
2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:						
<div style="margin-left: 40px;"> <input type="checkbox"/> the statement specified in 37 CFR 1.97(e); </div>						
OR						
<div style="margin-left: 40px;"> <input type="checkbox"/> the fee set forth in 37 CFR 1.17(p). </div>						

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(f))

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Title: **METHOD OF ENCAPSULATING SEMICONDUCTOR DEVICES ON A PRINTED CIRCUIT BOARD, AND
A PRINTED CIRCUIT BOARD FOR USE IN THE METHOD**

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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VOLENTINE FRANCO & WHITT, PLLC
11951 FREEDOM DRIVE, SUITE 1260
RESTON VA 20190
TEL. 571.283.0720

CC:

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